CLAIMS

What is claimed is:

1	1.	method of passivating an integrated circuit (IC), the method	
2	comprising:		
3		forming an insulating layer on said IC;	
4		forming an adhesion layer on a surface of said insulating layer by	
5	treating said	surface of said insulating layer with a gas; and,	
6		forming a first passivation layer upon said adhesion layer, said first	
7	passivation :	layer and said gas including at least one common chemical element.	
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	2.	The method of claim 1 further comprising forming a second	
2	passivation layer upon said first passivation layer.		
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1	3.	The method of claim 1 wherein said insulating layer includes silicon	
2:	dioxide.		
i.			
ű	4.	The method of claim 1 wherein said gas reacts with said surface of said	
2 :≟	insulating layer.		
1	5.	The method of claim 1, wherein said gas includes nitrous oxide (N20).	
1	6.	The method of claim 1, wherein said gas includes one of oxygen and	
2	nitrogen (N), and oxygen and ammonia (NH3), and oxygen and argon (Ar), and		
3	ozone (O ₃) and argon.		
1	7.	The method of claim 5, wherein said adhesion layer includes silicon	
2	oxynitride (Six0yNz).		
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1	8.	The method of claim 7, wherein said first passivation layer includes
2	silicon nitric	de. \
1	9.	The method of claim 1, wherein said first passivation layer is deposited
2	upon said ac	lhesion layer by way of a process of plasma enhanced chemical vapor
3	deposition (I	PECVD).
1	10.	The method of claim 8, wherein said at least one chemical element
2	includes nitr	rogen (N).
1	11.	The method of claim 2 wherein said second passivation layer includes
2	polyimide.	
1	12.	A method of passivating an integrated circuit (IC), the method
1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	comprising:	
<u>آ</u>		forming an oxide layer on said IC;
#re= 4#: .E.a.		forming an adhesion layer on a surface of said oxide layer by treating
!** \$i	said surface	of said oxide layer with nitrous oxide gas; and
5 11 6 11 11 11 11 11 11 11 11 11 11 11 1		forming a first passivation layer of silicon nitride upon said adhesion
7	layer.	
1	13.	The method of claim 12 turther comprising forming a second
2	passivation l	ayer upon said first passivation layer.
1	14.	The method of claim 12, wherein said adhesion layer includes silicon
2	oxynitride.	
1	15.	The method of claim 12, wherein said first passivation layer of silicon
2	nitride is dep	posited upon said adhesion layer by way of a process of plasma enhanced
3	chemical var	por deposition (PECVD).

1	16.	The method of claim 13, wherein said second passivation layer
2	includes pol	yimide /
1/	17.	An integrated circuit (IC) comprising:
		an insulating layer;
3		an adhesion layer formed over said insulating layer; and,
4		a first passivation layer formed on said adhesion layer, said first
5	passivation 1	layer and said adhesion layer including at least one common chemical
6	element.	
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1 	18.	The integrated circuit of claim 17 further comprising a second
2	passivation 1	ayer formed upon said first passivation layer.
IJ,	19.	The integrated circuit of claim 17 wherein said insulating layer
Y	includes sili	con dioxide (Si0 ₂).
としい	includes sili	
	includes silic	The integrated circuit of claim 17 wherein said adhesion layer includes
		The integrated circuit of claim 17 wherein said adhesion layer includes
	20. silicon oxyn	The integrated circuit of claim 17 wherein said adhesion layer includes itride.
	20. silicon oxyn 21.	The integrated circuit of claim 17 wherein said adhesion layer includes itride. The integrated circuit of claim 17 wherein said first passivation layer
	20. silicon oxyn 21.	The integrated circuit of claim 17 wherein said adhesion layer includes itride.
	20. silicon oxyn 21.	The integrated circuit of claim 17 wherein said adhesion layer includes itride. The integrated circuit of claim 17 wherein said first passivation layer
	20. silicon oxyn 21. includes sili	The integrated circuit of claim 17 wherein said adhesion layer includes itride. The integrated circuit of claim 17 wherein said first passivation layer con nitride (Si3N4). The integrated circuit of claim 18 wherein said second passivation layer
	20. silicon oxyn 21. includes silicon 22. includes pol	The integrated circuit of claim 17 wherein said adhesion layer includes itride. The integrated circuit of claim 17 wherein said first passivation layer con nitride (Si3N4). The integrated circuit of claim 18 wherein said second passivation layer yimide.
	20. silicon oxyn 21. includes sili	The integrated circuit of claim 17 wherein said adhesion layer includes itride. The integrated circuit of claim 17 wherein said first passivation layer con nitride (Si3N4). The integrated circuit of claim 18 wherein said second passivation layer yimide. An integrated circuit comprising:
	20. silicon oxyn 21. includes silicon 22. includes pol	The integrated circuit of claim 17 wherein said adhesion layer includes itride. The integrated circuit of claim 17 wherein said first passivation layer con nitride (Si3N4). The integrated circuit of claim 18 wherein said second passivation layer yimide. An integrated circuit comprising: a silicon dioxide insulating layer;
	20. silicon oxyn 21. includes silicon 22. includes pol	The integrated circuit of claim 17 wherein said adhesion layer includes itride. The integrated circuit of claim 17 wherein said first passivation layer con nitride (Si3N4). The integrated circuit of claim 18 wherein said second passivation layer yimide. An integrated circuit comprising:



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a silicon nitride hard passivation layer formed on said silicon oxynitride adhesion layer.

1	24. The integrated circuit passivation layer of claim 23 further comprising
2	a photodefinable polyimide soft passivation layer formed on said silicon nitride
3	hard passivation layer.

- 25. A method of passivating a trench on a semiconductor substrate, comprising the steps of:
 - forming a least one trench;
 - forming an insulating layer on said at least one trench;

forming an adhesion layer on a surface of said insulating layer by treating said surface of said insulating layer with a gas; and,

forming a first passivation layer upon said adhesion layer, said first passivation layer and said gas including at least one common chemical element.

- 26. The method of claim 25, wherein said gas reacts with said surface of said insulating layer.
- 27. The method of claim 25, wherein said gas includes nitrous oxide (N2O).
- 28. A method of passivating spacers, the method comprising the steps of:
- forming at least one spacer;
 - forming an insulating layer on said at least one spacer;
- forming an adhesion layer on a surface of said insulating layer by treating said surface of said insulating layer with a gas; and,
- forming a first passivation layer upon said adhesion layer, said first
 passivation layer and said gas including at least one common chemical element.

- 29.
- The method of claim 28, wherein said gas reacts with said surface of said insulating layer.
- The method of claim 28, wherein said gas includes nitrous oxide 30. 1
- $(N_2O).$ 2

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